

**AMENDMENTS TO THE SPECIFICATION**

The following amendment is made with respect to the application as-published in U.S. Patent Application Publication No. US 2004/0057460.

Please replace paragraph [0029] with the following replacement paragraph:

--[0029] Referring to FIG. 3, the RLC 100 is provided with different SAPs for access to the upper layer, such as T-SAP, UNACK-SAP and ACK-SAP. The [[z,900 LC]] RLC control[[□]] between the upper layer and the RLC 100 will be explained in detail. The entities 110, 120 and 130 in the RLC 100 have different forms of data transfer modes and functions. The RLC-transparent (RLC-T) entity 110 controls a data flow to logic channels, such as SCCH, BCCH, PCCH and DTCH, through a logic channel SAP connected to the MAC. The RLC-T 110 is provided with both a segmentation block 111 and a transmitter buffer block 112 in an up-link from the UTRAN and, as will be explained later, a RLC-T 210 in FIG. 4 is also provided with a segmentation block 211 and a transmitter buffer block 212 in a down-link from the UE. The RLC-T 110 is also provided with a reassembly block 113 and a receiver buffer block 114 in a down-link from the UTRAN, and RLC-T 210 has a reassembly block 213 and a receiver buffer block 214 in an up-link from the UE.--